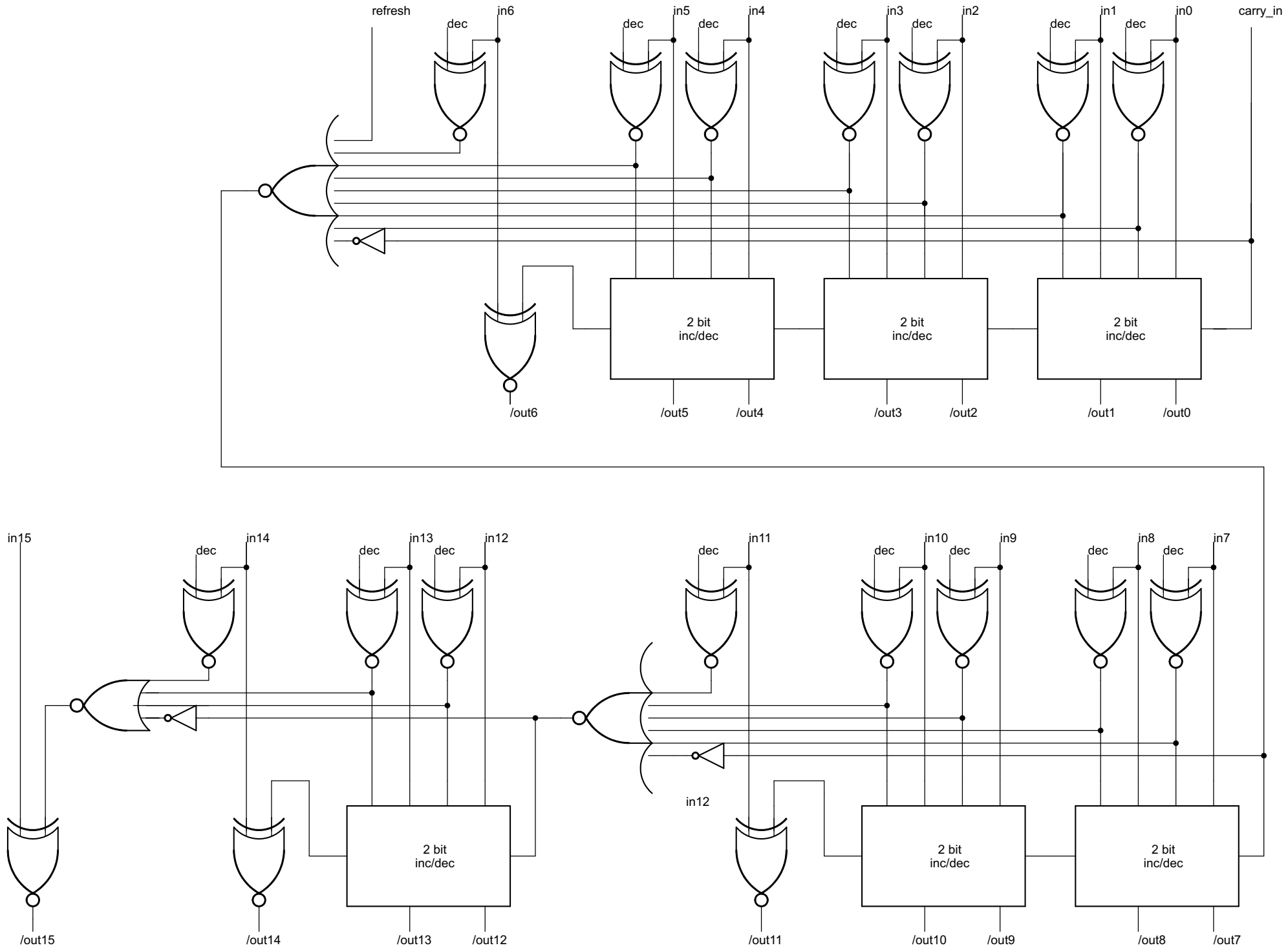
refresh
dec
in6
dec
in5
dec
in4
dec
in3
dec
in2
dec
in1
dec
in0
carry_in
2 bit
inc/dec
2 bit
inc/dec
2 bit
inc/dec
/out6
/out5
/out4
/out3
/out2
/out1
/out0
in15
dec
in14
dec
in13
dec
in12
dec
in11
dec
in10
dec
in9
dec
in8
dec
in7
in12
2 bit
inc/dec
2 bit
inc/dec
2 bit
inc/dec
/out15
/out14
/out13
/out12
/out11
/out10
/out9
/out8
/out7